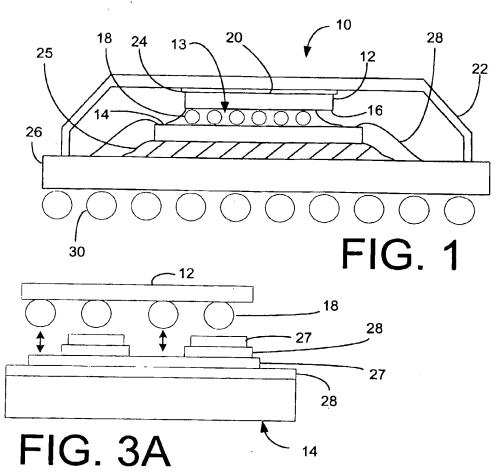
IMPROVED INTEGRATED CHIP PACKAGE AVING INTERMEDIATE SUBSTRA-SEHAT SUTARDJA MP0115 SHEET 1/3



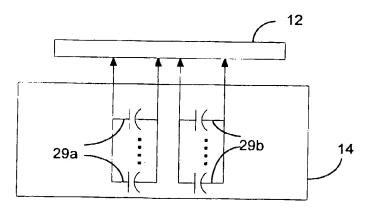
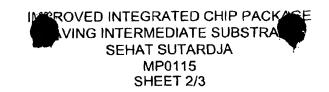


FIG. 3B



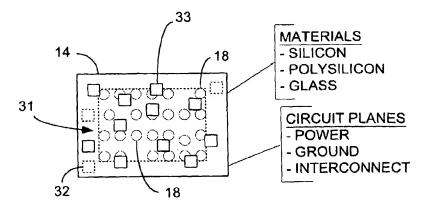
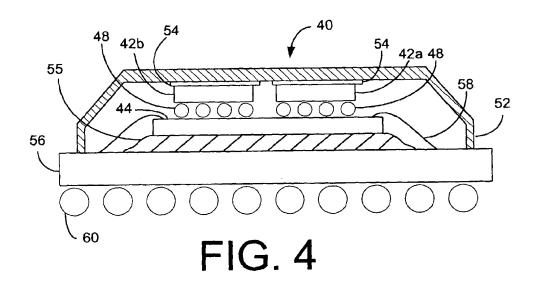


FIG. 2



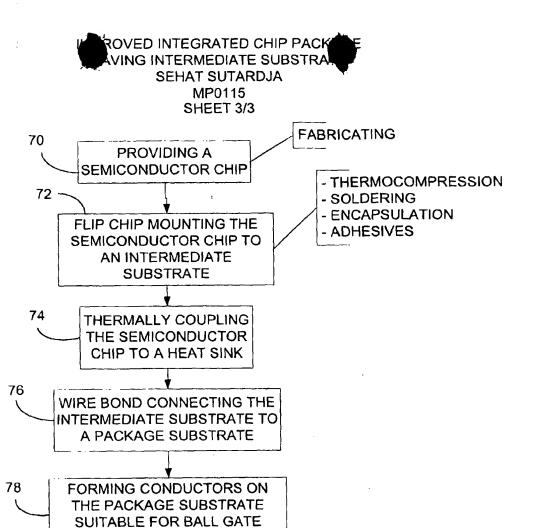


FIG. 5

ARRAY MOUNTING